

Notice of Allowability	Application No.	Applicant(s)	
	09/960,501	KAKEGAWA, MAKOTO	
	Examiner	Art Unit	
	Sun J Lin	2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to RCE filed on 02/11/2004.
2. The allowed claim(s) is/are 2-11,13-15,18-27,29-31,34-43,45-47,50-59,61-63 and 65.
3. The drawings filed on 09/24/2001 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date _____.
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

Examiner's Amendment

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Applicant's attorney Mehdi D. Sheikerz gave authorization for this examiner's amendment on March 2, 2004. The application has been amended as follows:

Claim 16 is cancelled without prejudice.

Claim 19, line 6, delete —generated—.

Reasons for Allowance

According to RCE filed on 02/11/2004 and Examiner's Amendment (03/02/2004), Claims 1, 12, 16, 17, 28, 32, 33, 44, 48, 49, 60 and 64 have been cancelled without prejudice.

Claims 2 – 11, 13 – 15, 18 – 27, 29 – 31, 34 – 43, 45 – 47, 50 – 59, 61 – 63 and 65 are allowed over the prior art of record. An examiner's statement of reasons for allowance is given in the following:

As to Claims 7, 11, 15, 23, 27, 31, 39, 43, 47, 55, 59 and 63, they are allowed due to the same reasons as included in *Office Action* mailed to applicant on 02/27/2003.

As to Claims 2 – 6, 8 – 10, 13, 14, 18 – 22, 24 – 26, 29, 30, 34 – 42, 45, 46, 50 – 54, 56 – 58, 61, 62 and 65, the prior art does not teach or suggest the following subject matters as recited in independent Claims 2, 3, 4, 5, 6, 18, 19, 20, 21, 22, 34, 35, 36, 37, 38, 50, 51, 52, 53, 54 and 65:

- A logical circuit designing device, comprising: a logical modification unit/means for modifying a logical circuit corresponding to a transmission line circuit according to a difference between the transmission line circuit edited by a transmission line editing unit and the logical circuit in combination with other limitations respectively recited in **Claims 2, 3, 18, 19**;
- A logical circuit designing device, comprising: a modifying means for modifying a logical circuit corresponding to a transmission line circuit according to a

difference between the transmission line circuit edited by transmission line editing means and the logical circuit in combination with other limitations respectively recited in **Claim 50**;

- A logical circuit designing program for enabling a computer, modifying a logical circuit corresponding to a transmission line circuit according to a difference between edited transmission line circuit and the logical circuit in combination with other limitations respectively recited in **Claims 34, 35**;
- A logical circuit designing device, comprising: a transmission line circuit generation unit/means for generating transmission line circuit data suitable for transmission line circuit analysis based on a stored logical circuit and information stored in a specified designation table in combination with other limitations respectively recited in **Claims 4, 5, 6, 52, 53, 54, 65**;
- A logical circuit designing method device, comprising: generating transmission line circuit data suitable for transmission line circuit analysis based on stored logical circuit and information stored in a specified designation table in combination with other limitations respectively recited in **Claims 20, 21, 22**;
- A computer program controlling a computer according to a process comprising: generating transmission line circuit data suitable for transmission line circuit analysis based on stored logical circuit and information stored in a specified designation table in combination with other limitations respectively recited in **Claims 36, 37, 38**;
- A logical circuit designing device, comprising: transmission line circuit generation means, storage means, edit means and modifying means in combination with other limitations recited in **Claim 51**.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sun J. Lin whose telephone number is (571) 272-1899. The examiner can normally be reached on Monday to Friday from 9:00am to 6:00pm.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

Sun J. Lin
Patent Examiner
Art Unit 2825
March 5, 2003


MATTHEW SMITH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800